Integrated Photonics Engineers

Job description

At Optalysys, we are designing cutting-edge encryption accelerator engines using integrated silicon photonics that will revolutionise data sharing and privacy enhancing technologies.

As a part of the existing photonics team, you will work with existing photonic systems and testbenches, and also design and optimise existing architectures, circuits and devices; and design next generation PICs. You will improve the performance of silicon photonic devices that are co-integrated with CMOS AMS circuit and digital logic.

You will work in a dynamic environment and should demonstrate exceptional organisational and technical skills to deliver mission critical circuit designs to meet tapeout deadlines. You will interact with world leading semiconductor foundries, the very best talents in tier 1 tech companies and other stakeholders.

Requirements

- Experience in testing and characterising photonic devices and optical transceivers.
- Experience in modelling and simulating photonic devices and transceiver technology.
- Experience in modelling photonic integrated circuits.
- Experience in characterisation of photonic devices and circuits.
- Experience in Lumerical, Python/MATLAB, Simulink.
- Excellent communication skills, including writing reports and presenting data to stakeholders.

The ideal candidate will also have:

- Experience in design and understanding of AMS driver circuits.
- Understanding of SerDes / DAC / TIA / ADC technologies.
- Experience in working with layout editors.
- Experience with Cadence Virtuoso design platform.

At Optalysys, we are a highly multidisciplinary team. The nature of our work means that professional growth in the role is guaranteed, regardless of background.

Note: This position will be involved with information protected under US export control laws and, therefore, the applicant must be compliant with these laws.

Contact

To apply for this role, please contact Dr Imon Kundu (imon.kundu@optalysys.com) with your CV and (optional) cover letter.